

CURRICULUM VITAE

Lekshmi Thankappan

Assistant Professor, Department of Electronics & Communication Engg.
Government Engineering College, Barton Hill, Under APJ Abdul Kalam Technological University.

EDUCATION

1. Level : **Post Graduate Degree(M.Tech)**
Field of study : **Micro Electronics & VLSI**
University : **Indian Institute of Technology, Madras**
CGPA : 8.86
Related Course Work : Digital Integrated Circuits, Semiconductor Device Modeling
MOS Device Modeling, Analog Integrated Circuits
Compound semiconductor Devices, MEMS

 2. Level : **Under Graduate Degree(B.Tech)**
Field of study : **Electronics Engineering**
University : **Cochin University of Science and Technology**
CGPA : 7.74
Related Course Work : Digital System Design, Advanced Integrated Circuits
Opto electronics and Communication, Microprocessor System
Design
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TEACHING

TEACHING INTERESTS

1. Solid State Devices
2. VLSI Design
3. VLSI Technology
4. Digital System Design
5. Mixed Signal Design
6. Analog Integrated Circuits

TEACHING EXPERIENCE

1. **June 2019- Present** : **Government Engineering College, Barton Hill,
Trivandrum , Under APJ Abdul Kalam Technological
University**
Designation : Assistant Professor
Department : Electronics and Communication

2. **Oct 2018-June 2019** : **Government Engineering College, Idukki, Under APJ
Abdul Kalam Technological University**
Designation : Assistant Professor
Department : Electronics and Communication

3. **March 2005-Oct 2013** : **Government Engineering College, Barton Hill,**

- Trivandrum , Under APJ Abdul Kalam Technological University**
- Designation** : Assistant Professor
Department : Electronics and Communication
4. **Feb 2004 – Feb 2005** : **College of Engineering, Thalassery, Cochin University of Science and Technology, India**
Designation : Lecturer
Department : Electronics and Communication
5. **July 2003 – Feb 2004** : **College of Engineering, Chengannur, Cochin University, India**
Designation : Guest Lecturer
Department : Electronics and Communication

PROFESSIONAL AND SERVICE RECORD

WORKSHOPS ORGANISED

1. Co-ordinated Short Term Programme titled “Recent Trends in VLSI and MEMS” from 8-14 August 2012 at GEC, Barton Hill under TEQIP -II.
2. Co-ordinated a STTP titled “Emerging Trends in Communication Engineering” at GEC, Barton Hill from 18th to 23rd March 2013 under TEQIP-II
3. Organised a two day workshop on “Matlab Hands on Training and Certification” under Equity Assurance scheme of TEQIP-II on 2nd and 3rd March 2013
4. Organised a Combined induction training Programme for 1st year students of ECE and Civil Engineering on 5th and 6th September 2013

RESEARCH

RESEARCH INTERESTS

1. Nanoelectronic devices
2. Digital Integrated circuits

CONFERENCE PAPERS

1. Paper entitled ”**Analysis and Modeling of SOI-HV-LDMOS Including self heating** “ at First National Conference on Emerging Technologies -2012 at GEC, Barton Hill on 20 and 21 July 2012
2. Presented a paper on “**Compact Modeling of SOI-LDMOS including quasi-saturation effect**”, at 2nd IEEE International Workshop on Electron Devices and Semiconductor Technology (IEDST 2009) at I.I.T. Bombay, June 1-2, 2009.